

WideVision: A Low-Power, Multi-Protocol Wireless Vision Platform for Distributed Surveillance

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Abstract— The trend in Internet of Things research points toward performing increasingly compute-intensive data analysis tasks on embedded sensor nodes, rather than server centers. Exploiting the technological advances in both energy efficiency, and Tiny Machine Learning algorithms and methods, an increasing number of recognition and classification tasks can be performed by small, low-power, wireless sensor nodes. This paper presents WideVision, a wireless, wide-area sensing platform capable of performing on-board person detection with power requirements in the mW range. The WideVision platform integrates seamlessly into the Internet of Things, by coupling a dedicated multi-radio platform, including a LoRa interface, enabling medium- and long-range communication, with a novel parallel RISC-V microcontroller. We evaluate the proposed platform with the GAP8 microcontroller, which includes an 8-core RISC-V cluster, and greyscale camera to perform person detection by training and deploying an advanced, quantized neural network, achieving a statistical accuracy 84.5% for a 5-person detection task with a latency of only 182 ms. Experimental results demonstrate that the WideVision sensor node platform while performing inference at a rate of one image per minute on-board, is capable of lasting 300 days on a 2400 mAh Li-ion battery, and 65 days when evaluating one image per 10 seconds while providing effective surveillance of its perimeter.

I. INTRODUCTION

In recent years, Internet of Things (IoT) devices have become more and more intelligent and pervasive in all areas of everyday life [1]. Enabled by low-power sensors and a high degree of technological integration, IoT devices cover a wide range of applications. The first generations of IoT devices stream the raw data they acquire to a centralized server, where useful information is extracted. This paradigm of cloud-centric computing has significant disadvantages: Since raw data is transmitted, the lion's share of power consumption is consumed by the radio interface of the system. Further, the latency of the system is dictated by the round-trip time between the node and the cloud. Especially with sensor networks that produce a large amount of rapidly changing data and require a short reaction latency, edge computing improves performance, battery lifetime, and cost. [2].

In edge computing, sensor nodes apply advanced algorithms, often from the domain of machine learning, to extract useful information from the raw data they acquire. Edge computing not only minimizes the amount of data sensor nodes have to transmit via radio, which significantly improves the latency of the system [3]. The new generation of edge computing-focused

Microcontrollers (MCUs) often features on-chip memory in the order of hundreds of kilobytes and can process sub-word data efficiently by exploiting specialized Instruction Set Architecture (ISA) extensions. Some modern MCUs even feature multi-core compute clusters, further decreasing the latency of Deep Neural Network (DNN) inference [4], [5]. Following the advancement of specialized MCUs, deployment of complex DNNs has become feasible without compromising the battery life of their host system, giving rise to the field of TinyML [5], [6].

Adapting surveillance tasks to this emerging paradigm of edge computing is quite challenging. Surveillance requires extensive area coverage by sensors, and accurate detection and classification to be effective, posing significant challenges for embedded systems [7], [8]. A distributed approach where many wide-area sensor nodes cover partial perimeters in combination with a few long-range nodes often works well in practice to adapt to occluded areas [7], [9]. Ideally, these nodes are battery-powered and able to communicate events quickly, to minimize both the required maintenance and reaction time.

This work proposes a novel system that tackles the challenges distributed wide-area and long-range sensor systems face, by designing and implementing a multi-radio platform. We further integrate this platform into a low-power surveillance system, using an off-the-shelf RISC-V multi-core MCU board and camera. By training and deploying a Convolutional Neural Network (CNN) we demonstrate that our platform achieves state-of-the-art energy efficiency for vision-based surveillance applications.

Experimental results with in-field measurements show that our solution has a connectivity range of several kilometers using the LoRa interface while sending few bytes of processed information such as the detection of a person. The sensor node detects and counts people with an accuracy of 84.5% and an inference latency of 182 ms. Experimental results show that the proposed solution can last 65 d in the field using an off-the-shelf 2400 mA h battery pack, enabling a long-lasting, decentralized sensing network.

II. RELATED WORK

In the field of smart sensor nodes, the topics of embedded Machine Learning and edge computing have seen numerous contributions in the past few years. A wide range of hardware

platforms and machine learning algorithms have been studied to enable deployable systems [10].

Distributed and embedded surveillance systems are well-researched, with many contributions focusing on a variety of application scenarios, ranging from agricultural to healthcare monitoring [9], [11]–[15]. While the body of work is fairly extensive, most of the proposed designs are either not optimized for ultra-low power battery-based operation [11], [12], [15] or too inflexible for reliable surveillance, due to strict constraints on their power consumption [13], [14]. Nevertheless, some recent research has investigated low-power, battery-operated distributed surveillance systems or similar concepts.

The authors in [7] propose a Wi-Fi-based wireless multimedia sensor network for IoT applications. To reduce the power consumption by the cameras, which stream their video output via Wi-Fi, the authors propose to use a Long Range (LoRa) network to control the activity of each node. Their solution is shown to reduce the system-level power consumption since power is consumed by fewer nodes at a time. In a fully embedded, battery-operated surveillance setting, however, this solution is largely infeasible, since if a camera is offline, some part of the perimeter is left unsupervised.

Benninger et al. [16] present a sensor node based on the GAP8 microcontroller, combined with two microphones, a camera, a light sensor, and a LoRa radio. By exploiting aggressive power optimization techniques, the authors reduce the average power consumption of the system to around $770 \mu\text{W}$, assuming one inference is performed per minute. While this system's power consumption is very impressive, it is unable to adapt to different latency and throughput requirements, since the only available radio interface is LoRa. Due to its highly integrated nature, it is also not possible to exchange the sensors or main processor.

The closest work to ours is presented by Polonelli et al. [17]. The authors propose a multi-radio shield featuring LoRa, Wi-Fi, and Bluetooth radio interfaces. While their system demonstrates flexibility and energy efficiency for what concerns the radio interface, the evaluation only considers the radio interface energy and does not consider edge computing aspects as well as deployment constraints and issues. Thus, in this paper, we focus on the tiny machine learning aspect and the long-range wireless interface using LoRa to send processed information to a remote gateway up to several km of distance.

We show on an end-to-end system that our approach reduces the energy cost of surveillance in an embedded setting by moving the computational burden on the sensor board and significantly reducing the energy cost of LoRa radio transmission, leading to state-of-the-art energy efficiency and accuracy in a low-power surveillance system.

III. SYSTEM ARCHITECTURE

In a typical sensor network systems application scenario, several sensor nodes communicate with a central gateway, forming a star topology network, as shown in Figure 1. Implementing

networking with a star topology saves energy since sensor nodes do not need to route packets. The range over which sensor nodes need to transmit data depends on the application scenario. In the context of highly distributed systems, the required distance might reach kilometer range [8], while other applications might only require tens or hundreds of meters [18]. Similarly, the throughput requirements for applications vary enormously. While some applications require continuous streaming of data, others only require transmitting compressed classification data.

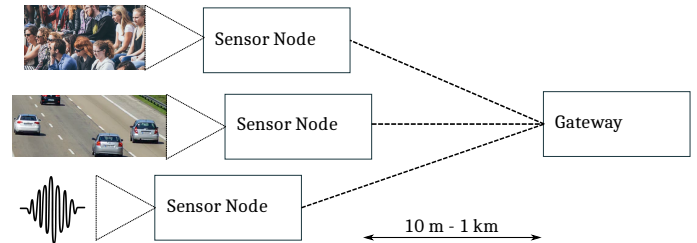


Fig. 1. High-level view of the application scenario. The sensor nodes monitor distinct areas and communicate with the gateway in a star topology network.

To enable a trade-off between range, throughput, latency, and transmission power, as required by different applications, we propose the design of a multi-radio platform, which can equip any off-the-shelf microcontroller with multi-protocol radio support, enabling both the streaming of data over short distances as well as the transmission of compact data packets over large distances. As shown in Figure 2, the sensor node consists of a sensor, a data processing unit, as well as the multi-radio platform. The processing unit extracts useful information and/or compresses the sensor data to reduce the transmitted size. The custom multi-radio platform has a standard Arduino UNO connector and provides wireless connectivity as well as a battery power circuit. It converts the host platform to an IoT capable sensor node supporting LoRa, Wi-Fi and Bluetooth. Each radio interface on the multi-radio platform is designed to enable different operating modes and deployment scenarios: While the LoRa interface allows communication in the range of multiple kilometers, the Bluetooth and Wi-Fi interfaces can be used to trade-off range for transmission rate and decreased latency.

In this work, we evaluate the LoRa radio interface in a distributed surveillance application scenario, with a special focus on minimizing the power consumption during idle periods.

IV. IMPLEMENTATION

In this section, we discuss the proposed multi-radio system and how we extend it by implementing a sensor node architecture for surveillance applications. Our implementation is capable of transmitting highly compressed information extracted from images over long distances, reaching up to 10 km.

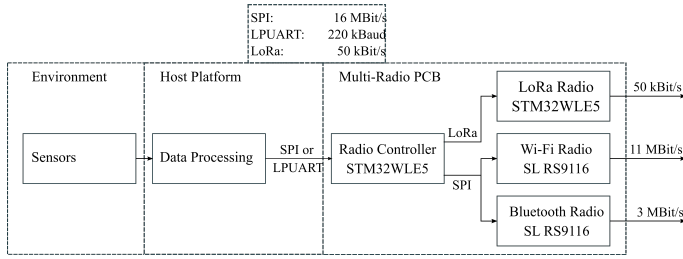


Fig. 2. Architecture of sensor nodes using the proposed multi-radio platform. Data Processing is performed on the host platform, while the multi-radio platform handles transmission of raw or compressed data. Depending on the application, the multi-radio receives data over SPI or UART.

A. MultiRadio Platform

The multi-radio platform is designed to both minimize transmission energy, as well as idle power consumption. We achieve this by implementing a dedicated DC/DC buck converter, which can be used to fully power gate the components on the PCB. Figure 3 shows a diagram of the PCB's schematics.

To power the sensor node, a 1S Lithium-ion battery is required. The battery is protected and charged by a dedicated circuit built around a Texas Instruments BQ24232HARGTR Integrated Circuit (IC). The multi-radio platform provides the 3.7 V supply of the Li-Ion battery directly to the host platform, while the radio interfaces and the radio controller are supplied with 2 V from a MAXM38643AEMB+ buck converter by Maxim Integrated. To reduce power consumption in idle mode, The buck converter can be completely powered off by the host platform.

This results in two distinct power domains on the multi-radio platform, one of which is powered by the battery voltage which is always on, and a switched 2 V power domain. To allow communication between any host platform and the 2 V logic on the multi-radio platform, logic level shifters are integrated on the multi-radio platform.

The processing unit on the host platform communicates over Serial Peripheral Interface (SPI) with the radio controller, an ARM Cortex M4 processor by ST microelectronics. We chose the STM32WLE5JC, since it integrates a LoRa radio with two transmitting power options and its power consumption of $72 \mu\text{A MHz}^{-1}$ is one of the lowest found in commercial off-the-shelf microcontrollers with integrated LoRa radio interface.

Additionally, a Silicon Labs RS9116-CC1 radio module is integrated for Wi-Fi and Bluetooth connectivity. The RS9116 is connected with the radio controller over an SPI interface, as shown in Figure 3. The radio controller is used to handle the protocol stack of all radio interfaces. This allows the sensor node to be fully decoupled in terms of processing and data transmission; The host microcontroller can be fully power-gated while the radio controller is still able to communicate with the gateway. Vice-versa, the radio controller can be fully power-gated when no communication is required. The setup with the STM32WLE5 as a single interface to the host platform

and the incorporation of logic level converters results in an energy-efficient and portable multi-radio platform.

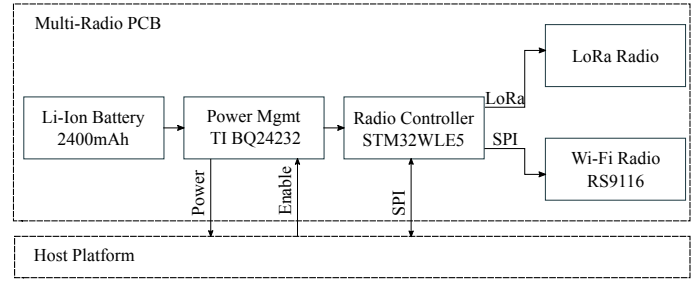


Fig. 3. Block diagram of the proposed multi-radio platform. The platform features a battery management circuit, used to power the host platform, and a DC/DC buck converter, which powers the Radio circuitry and enables the host platform to power off the radio controller and its interfaces.

B. Sensor Node

We implemented a full sensor node using the GAPuino evaluation board. A GAP8 development platform with an integrated camera connector is used as the host platform for sensor data processing. We use the Himax HM01B0 embedded camera, a state-of-the-art embedded low-power QVGA greyscale camera. The custom multi-radio platform is used to provide the sensor node with Long Range Wide Area Network (LoRaWAN) connectivity and power. In this setup, the GAPuino is the host platform for the multi-radio platform. A schematic of this setup is depicted in Figure 4 and a picture of the complete sensor node is shown in Figure 5.

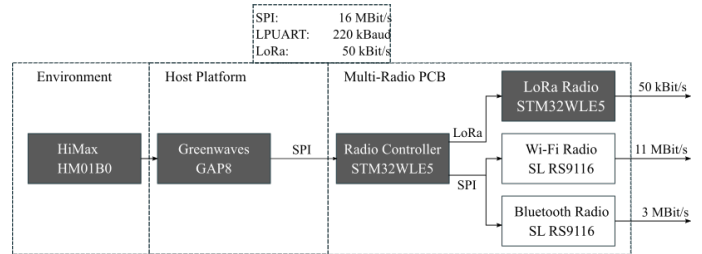


Fig. 4. High level schematic of a sensor node used for evaluation. An embedded grayscale camera is used as an image sensor and the GAPuino's octa-core cluster is used for machine-learning image classification.

C. Neural Network

One of the contributions of this work is the training, quantization, and deployment of a CNN used for person detection on the host platform. We propose a model that minimizes the memory and computational resource usage of the GAP8 microcontroller, which typically poses the greatest challenge for deployment of neural networks on small embedded devices, while still offering the required accuracy for surveillance applications. The GAPuino board used in this implementation is a development board designed around the GAP8 microcontroller,

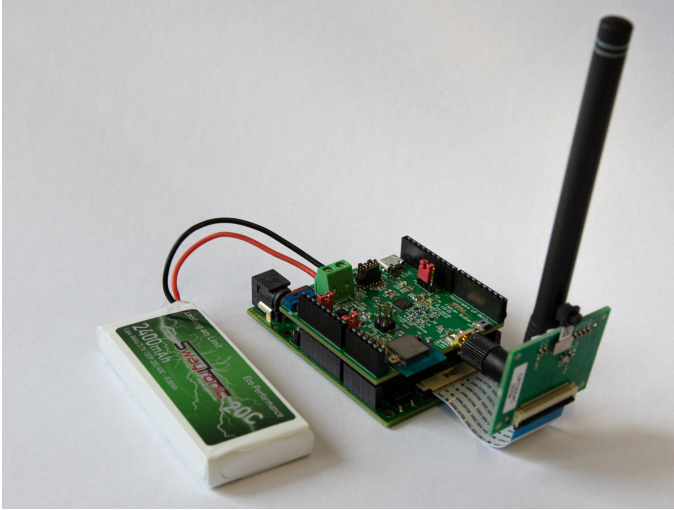


Fig. 5. Picture of the assembled surveillance sensor node, consisting of an off-the-shelf Li-Ion battery, the GAPuino, the Himax camera, as well as the custom multi-radio platform.

and octa-core RISC-V microcontroller featuring 512 kB of on-chip memory, based on the PULP SoC [19]. Besides this microcontroller, the GAPuino also features 16 MB of external HyperRAM Dynamic Random Access Memory (DRAM) memory, which is used for storage of model weights and intermediate feature maps in large models.

a) Model architecture & Training: The proposed model architecture is a modified version of MobileNet-v1 [20]. Similar to the original architecture, we use 3×3 Depthwise-separable convolutions with strides to reduce the feature map dimensions and reduce the computational and memory requirements compared to dense convolutions. To ease quantization, each convolution is followed by a batch normalization layer [21] and a ReLU activation [22]. Table I lists the model parameters. The model classifies each input image and outputs the number of people in the field of view in 5 classes, including classes for 0-3 people and a class for more people. We used the Adam optimizer [23] with an initial learning rate of 10^{-3} and a learning rate scheduler that reduces the learning rate by $3 \times$ if the validation loss has not improved for 10 epochs.

b) Dataset & Preprocessing: The use of large, sensor-specific datasets is an important factor for the successful deployment of machine learning models to embedded systems. Since such datasets are rarely openly accessible, and to help with the comparability of our solution, we used the COCO dataset [24] to train our model. Since the COCO dataset uses high-resolution images annotated with bounding boxes that locate the position of objects, including people, we used a custom pre-processing pipeline to rescale the images and annotate them with the classification labels required by our network. We first apply a random rescaling of the image, randomly crop a patch of size 320×240 , apply RandAugment [25] with parameters $N = 2$, $M = 14$ and convert the image

to greyscale.

c) Quantization & Deployment: To execute the model on the GAP8 platform, it needs to be quantized, such that it can be executed using only integer arithmetic since the GAP8 does not have a Floating Point Unit (FPU). Quantizing the model's weights and activations to 8 bit further helps to drastically reduce the memory requirements for storing model weights and feature maps as well as the compute requirements since the ISA extensions of the RI5CY cores inside the GAP8 enable parallel Single Instruction Multiple Data (SIMD) processing. We used the Quantlib¹ framework to quantize both the weights and activations of the network to 8 bit, and the Dory framework [5] to deploy it on the GAP8 microcontroller.

d) Evaluation: The model has a total size of 90.5 kB and requires 103.6 MOps per inference. Deploying it on GAP8 and running on all 8 cores in the cluster, the inference takes a total of 18.2 MCycles, achieving an average compute intensity of 5.45 Op/Cycle. On the GAP8 running at 100 MHz, inference takes 182 ms. The model achieves an average accuracy of 84 % on the validation dataset.

TABLE I
MODEL ARCHITECTURE OF THE PROPOSED NEURAL NETWORK

Layer	Input	Stride	Channels
Dense 2D Convolution	320×240	2	32
Separable 2D Convolution	160×120	2	48
Separable 2D Convolution	80×40	1	64
Separable 2D Convolution	80×40	2	64
Separable 2D Convolution	40×20	1	64
Separable 2D Convolution	40×20	2	64
Separable 2D Convolution	20×10	2	96
Separable 2D Convolution	10×5	2	96
Separable 2D Convolution	5×3	2	128
Linear Layer	768	-	5

V. POWER AND ENERGY CONSUMPTION ANALYSIS

To test the functionality and measure the performance of our proposed platform, we use the sensor node system described in Section IV, consisting of the multi-radio platform and the GAPuino microcontroller board. We measure the idle power consumption, as well as the total energy cost of taking an image, processing it using the neural network, and transmitting the classification results with the LoRa radio interface. We further calculate the energy cost of transmitting a raw image, to compare our approach. Finally, we estimate the overall system lifetime using the attached battery depending on the frequency with which images are processed.

To evaluate the contribution of each device individually, the power consumption of the multi-radio platform, GAP8 processor, and HiMAX camera were measured separately and added afterward.

¹<https://github.com/pulp-platform/quantlib>

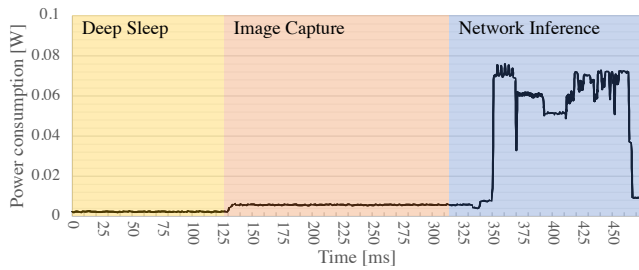


Fig. 6. Power measurement of image capture and neural network inference on the GAPuino board.

A. GAPuino Evaluation Setup

To evaluate the efficiency of the host processor and camera sensor, we measure the power consumption of the GAPuino board, as used in our prototype system. Since the GAPuino is equipped with non-optimal Low Dropout Regulators (LDOs) that convert the battery voltage of 3.7 V to the operation voltage of 1.2 V, leading to an efficiency loss of at least 66 %, we only measure the power consumption of the GAP8 processor and the Himax camera, both while idle as well as during inference and image acquisition. We use the measured power consumption of the processor and camera to estimate the energy cost of acquiring, analyzing, and transferring an image.

B. MultiRadio Evaluation Setup

To measure the power consumption during startup, LoRaWAN joining sequence, transmission, and reception of the multi-radio platform the Printed Circuit Board (PCB) was powered using a Keysight N6715C DC power analyzer. The power analyzer provides 3.7 V to the multi-radio platform over the Li-ion battery connector and monitors the current consumption. In our measurement setup, the multi-radio platform transmits a LoRa package with 16 B of payload to the gateway using DR0 and DR5, respectively² (SF8, 125 kHz bandwidth and 3125 bit s^{-1}) at 18 dBm output power. This accurately models the payload size of our implementation, since we only transmit the device ID, a timestamp, and the inference result, which total 6 B. However, due to the AES encryption used in the LoRaWAN protocol, the minimum payload size is 16 B. Figure 7 shows the power trace of the multi-radio PCB for a transmission of a classification result with DR0. Using these measurements, we consider the packet TX window and one RX window as the basis for our energy consumption calculations.

C. Power Measurement Results

To estimate the lifetime of the sensor node, we profiled both the idle power consumption of the sensor node and the multi-radio platform, as well as the energy cost of acquiring an image, running the neural network, and transmitting the classification

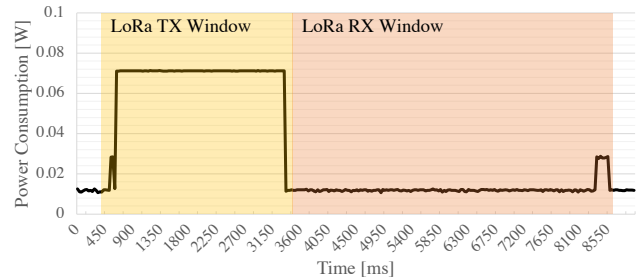


Fig. 7. Power measurement of the multi-radio PCB during transmission via the LoRa interface at DR0, at 18 dBm transmission power.

result with the LoRa interface. With the processor in deep-sleep mode, the camera turned off, and the real-time clock turned on, we measured a power consumption of $156 \mu\text{W}$ on the GAPuino board. With the DC/DC converter on the multi-radio platform turned off, we measured a power consumption of $270 \mu\text{W}$ from the battery. Hence, while in deep sleep, waiting to acquire and process another image, the platform consumes $426 \mu\text{W}$. The average power consumption of the GAP8 processor during inference is 51 mW at 100 MHz , leading to a total energy cost per inference of 9.5 mJ . The acquisition and transfer of an image require around 1 mJ . Figure 6 shows the power trace of one image acquisition and neural network inference on the GAPuino board. The transmission of a classification result at 18 dBm using the DR0 mode of the LoRa interface consumes around 240 mJ over 8.5 s . In DR5 mode, the transmission time is reduced to 5.2 s , resulting in an energy cost of 61.4 mJ . When using DR0 for data transmission, the overall energy cost of image acquisition, inference, and transmission comes up to 250.5 mJ , in DR5 mode the overall energy cost comes up to 72 mJ .

D. Lifetime Estimation

Using the measurements presented in the previous section, we model the energy consumption by computing the average power consumption depending on the ratio between deep-sleep and active mode. We further calculate the energy in the fully charged battery to be 35964 J , assuming a capacity of 2400 mAh at 3.7 V . Assuming a high image processing frequency of 1 image per 10 seconds and every inference triggering a transmission, our platform can last at least 16.7 d . Assuming one image is processed every 60 seconds, our platform can last for at least 92 d . When operating on a shorter range, where transmissions using the DR5 mode are possible, these lifetime estimates increase to 65 d and 300 d , respectively.

E. Comparison With Related Work

When directly comparing our results to [16], we find that our estimate for the system's lifetime is 100 d , or 25 % lower. This can be explained by two aspects of our implementation and evaluation. Firstly, our neural network is more complex,

²<https://loro-developers.semtech.com/documentation/tech-papers-and-guides/loro-and-lorawan/>

which leads to higher energy costs for inference. Secondly, the authors in [16] do not consider the energy cost of receiving an answer from the gateway during the RX window of LoRa transmissions, which neglects a large amount of the real energy cost of running a practical radio system.

We further compare with the work by Polonelli et al. [17], which reports energy per Bit numbers for transmission with LoRa. In our evaluation, we find that our transmission energy cost per Bit in DR0 is about 5 mJ, 44 % lower than their solution, at an equal LoRa TX power of 20 dBm. The energy cost per Bit figure in [17] adds up the average power of the antenna, the SemTech 1276 chip, and communication between their microcontroller and SemTech chip. Since our solution combines the LoRa frontend and microcontroller, we avoid costly inter-chip communication, which explains the increased efficiency of our solution.

VI. CONCLUSION

This paper presents a multi-radio platform for IoT applications, that aims to easily convert any microprocessor into an IoT capable sensor node with a battery power circuit. The multi-radio platform is evaluated in a wide-area surveillance application, using a GAPAino development board featuring the GAP8 low-power RISC-V multicore processor. A CNN for person recognition was trained and deployed on the GAPAino. The power consumption of both the machine learning and the data transmission over LoRaWAN were measured, and estimates for the sensor lifetime were calculated, based on measurements of the system's idle and active power. We show that our system can last 300 d on a single battery charge while performing accurate surveillance in the field.

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